



European Research Consortium  
for Informatics and Mathematics

# Call For Participation

15th International Workshop on Formal Methods  
for Industrial Critical Systems (FMICS 2010)  
September 20-21, 2010, Antwerp, Belgium  
<http://es.fbk.eu/events/fmics2010>

## Co-chairs:

Stefan Kowalewski (RWTH Aachen University)  
Marco Roveri (FBK-irst)

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Aarti Gupta (NEC Labs)  
Andreas Podelski (University of Freiburg)  
Andy King (Portcullis Computer Security)  
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Wan Fokkink (Vrije Universiteit Amsterdam)  
Wilfried Steiner (TTTech)

## ERCIM FMICS WG Coordinator:

Alessandro Fantechi (Univ. Firenze, Italy)

## Sponsored by:

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## Published by:



## Scope of the Workshop:

The aim of the FMICS workshop series is to provide a forum for researchers who are interested in the development and application of formal methods in industry. In particular, these workshops bring together scientists and engineers that are active in the area of formal methods and interested in exchanging their experiences in the industrial usage of these methods. These workshops also strive to promote research and development for the improvement of formal methods and tools for industrial applications.

## Invited Speakers:

This year, FMICS will feature four outstanding invited speakers - two outstanding academic scientists and two prominent industrialists working in top companies with emphasis on formal methods for critical systems.

- Aarti Gupta, NEC Labs USA.
- Axel Simon, Technical University of Munich, Germany.
- Bert van Beek, Technical University of Eindhoven, The Netherlands.
- Stephan Tobies, European Microsoft Innovation Center, Aachen, Germany.

## List of Accepted Papers:

- Wilfried Steiner and Bruno Dutertre: "SMT-Based Formal Verification of a TTEthernet Synchronization Function".
- Lars Kristensen and Michael Westergaard: "Automatic Structure-based Code Generation from Coloured Petri Nets: A Proof of Concept".
- Frank Werner and David Farag: "Correctness of Sensor Network Applications by Software Bounded Model Checking".
- Ahmed Mashiyat, Fazle Rabbi, Hao Wang and Wendy MacCaull: "An Automated Translator for Model Checking Time Constrained Workflow Systems".
- Despo Galataki, Andrei Radulescu, Kees Verstoep and Wan Fokkink: "Embedded Network Protocols for Mobile Devices".
- Alessio Ferrari, Alessandro Fantechi, Daniele Grasso and Gianluca Magnani: "The Metro Rio ATP case study".
- Wei Wei and Jeremy W. Bryans: "Formal analysis of BPMN models using Event-B".
- Joerg Brauer, Andy King and Stefan Kowalewski: "Range Analysis of Microcontroller Code using Bit-Level Congruences".
- Michael Althenhofen and Achim D. Brucker: "Practical Issues with Formal Specifications Lessons Learned from an Industrial Case Study".
- Radu Mateescu and Wendelin Serwe: "A Study of Shared-Memory Mutual Exclusion Protocols using CADP".
- Michael Gerke, and Rdiger Ehlers, Bernd Finkbeiner and Hans-Jrg Peter: "Model Checking the FlexRay Physical Layer Protocol".
- Christian Kern and Javier Esparza: "Automatic Error Correction of Java Programs".
- Sebastian Mdersheim, Dieter Sommer and Jan Camenisch: "A Formal Model of Identity Mixer".
- Alexei Iliasov, Elena Troubitsyna, Linas Laibinis, Alexander Romanovsky, Kimmo Varpaaniemi, Dubravka Ilic and Timo Latvala: "Developing mode-rich satellite software by refinement in Event B".